

JCxxxx-XFP-LC.S40

**CWDM XFP Single-Mode for 10GbE/10GFC Duplex
XFP Transceiver
RoHS6 Compliant**



Features

- ◆ Supports 9.95Gb/s to 11.1Gb/s Bit Rates
- ◆ Hot-Pluggable XFP Footprint
- ◆ Compliant with XFP MSA
- ◆ 10-Wavelengths CWDM DFB Transmitter from
1270nm to 1450nm, with Step 20nm
- ◆ 14dB power budget at least
- ◆ Duplex LC Connector
- ◆ Power Dissipation < 2.5W
- ◆ Case Operation Temperature Range
-5°C to 70°C
- ◆ 2-Wire Interface for Integrated Digital
Diagnostic Monitoring

Applications

- ◆ 10GBASE ER/EW 10G Ethernet
- ◆ 1200-SM-LL-L 10G Fiber Channel
- ◆ 10GE over G.709 at 11.09Gbps

Ordering Information

Part No.	Data Rate	Laser	Fiber	Distance	Interface
JCxxxx-XFP-LC.S40*(note)	10G	CWDM DFB	SMF	14dB power budget	LC

Note1: X refers to CWDM Wavelength range 1270nm to 1450nm, X=A to J, for detailed definition, please refer the following table.

CWDM Wavelength*Note2

Band	Nomenclature	Wavelength(nm)		
		Min.	Typ.	Max.
O-band Original	1270	1264	1270	1277.5
	1290	1284	1290	1297.5
	1310	1304	1310	1317.5
	1330	1324	1330	1337.5
	1350	1344	1350	1357.5
E-band Extended	1370	1364	1370	1377.5
	1390	1384	1390	1397.5
	1410	1404	1410	1417.5
	1430	1424	1430	1437.5
	1450	1444	1450	1457.5

Note2: This wavelength is supported with limited availability; please contact Data Controls Inc's for further details.

Regulatory Compliance

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12
		EN 60825-1:2007
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50135430 0001	EN 55022:2006
		EN 55024:1998+A1+A2
CB	JPTUV-024038-M1	IEC 60825-2
		IEC 60950-1
FCC	WTF13F0503735E	47 CFR PART 15 OCT., 2010
	WTF13F0503732E	47 CFR PART 15 OCT., 2010
FDA	1230816-000	CDRH 1040.10
ROHS	RLSZF00163462	2011/65/EU

Product Description

The JCxxx-XFP-LC.S40 series optical transceiver is designed for fiber communications application such as 10G Ethernet (10GBASE-ER/EW) and 10G Fiber Channel (1200-SM-LL-L), which fully compliant with the specification of XFP MSA Rev 4.5.

This module is designed for single mode fiber and operates at a nominal wavelength of CWDM wavelength. There are 10 center wavelengths available from 1270nm to 1450nm, with each step 20nm. A guaranteed optical link budget of 14dB is offered.

The module is with the XFP 30-pin connector to allow hot plug capability. Only single 3.3V power supply is needed. The optical output can be disabled by LVTTTL logic high-level input of TX_DIS.

Loss of signal (RX_LOS) output is provided to indicate the loss of an input optical signal of receiver. This module provides digital diagnostic functions via a 2-wire serial interface as defined by the XFP MSA Rev 4.5.

Absolute Maximum Ratings

Parameter	Symbol	Min	Typical	Max	Unit	Note
Maximum Supply Voltage	V _{cc}	-0.5		4.0	V	
Storage Temperature	T _s	-40		85	°C	

Recommend Operating Condition

Parameter	Symbol	Min	Typical	Max	Units	Note
Operating Temperature	T _c	-5		70	°C	
Supply Voltage	V _{cc}	3.13	3.3	3.45	V	
Supply Current	I _{cc}			750	mA	
Module Total Power	P			2.5	W	

Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Note
Transmitter						
Input Differential Impedance	R _{in}		100		Ω	1
Differential Data Input Swing	V _{in} , pp	180		820	mV	
Transmit Disable Voltage	V _{DIS}	2.0		V _{cc}	V	
Transmit Enable Voltage	V _{EN}	GND		GND+ 0.8	V	
Transmit Disable Assert Time				10	us	
Receiver						
Differential Data Output Swing	V _{out} , pp	340	650	850	mV	
Data Output Rise Time	t _r			38	ps	2
Data Output Fall Time	t _f			38	ps	2
LOS Fault	V _{LOS fault}	V _{cc} - 0.5		V _{cc HOST}	V	3
LOS Normal	V _{LOS norm}	GND		GND+0.5	V	3
Power Supply Rejection	PSR		See Note 4 below			4

Notes:

1. After internal AC coupling.
2. 20 – 80 %.
3. Loss of Signal is open collector to be pulled up with a 4.7k – 10kohm resistor to 3.15 – 3.6V.
Logic 0 indicates normal operation; logic 1 indicates no signal detected.
4. Reference the Section 2.7 of the XFP MSA Rev4.5.

Optical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Note
Transmitter						
Output Opt. Pwr: 9/125 SMF	P _{out}	-1.8		+4	dBm	1
Optical Extinction Ratio	ER	3.5			dB	
Optical Wavelength	λ	$\lambda_c - 6$	λ_c	$\lambda_c + 7.5$	nm	2
-20dB Spectrum Width	$\Delta\lambda$			1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Path Penalty	P _p			2	dB	
Average Launch Power of OFF Transmitter	P _{OFF}			-30	dBm	
TX Jitter	TX _j	Per 802.3ae requirements				
Relative Intensity Noise	RIN			-128	dB/Hz	
Receiver						
Receiver Sensitivity @ 10.7Gb/s	P _{min}			-15.8	dBm	3
Maximum Input Power	P _{max}	-1			dBm	
Optical Center Wavelength	λ	1260		1600	nm	
Receiver Reflectance	R _{rf}			-12	dB	
LOS De-Assert	LOS _D			-17.8	dBm	
LOS Assert	LOS _A	-29.8			dBm	
LOS Hysteresis		1			dB	

Notes:

- Output is coupled into a 9/125 μ m SMF.
- ITU-T G.694.2 CWDM wavelength from 1270nm to 1450nm, each step 20nm.
- Average received power; BER less than 1E-12 and PRBS 2³¹-1 testpattern.

Pin Descriptions

Pin	Logic	Symbol	Name/Description	Notes
1		GND	Module Ground	1
2		VEE5	Optional -5.2 Power Supply – Not Required	
3	LVTTL-I	Mod-Desel	Module De-select; When held low allows the module to respond to 2-wire serial interface commands	
4	LVTTL-O	/Interrupt	/Interrupt; Indicates presence of an important condition which can be read over the serial 2-wire interface	2
5	LVTTL-I	TX_DIS	Transmitter Disable; Transmitter laser source turned off	

6		VCC5	+5 Power Supply - Not Required	
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTL-I	SCL	Serial 2-wire interface clock	2
11	LVTTL-I/O	SDA	Serial 2-wire interface data line	2
12	LVTTL-O	Mod_Abs	Module Absent; Indicates module is not present. Grounded in the module.	2
13	LVTTL-O	Mod_NR	Module Not Ready;	2
14	LVTTL-O	RX_LOS	Receiver Loss of Signal indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver inverted data output	
18	CML-O	RD+	Receiver non-inverted data output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply – Not required	
21	LVTTL-I	P_Down/RST	Power Down; When high, places the module in the low power stand-by mode and on the falling edge of P_Down initiates a module reset	
			Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.	
22		VCC2	+1.8V Power Supply – Not required	
23		GND	Module Ground	1
24	PECL-I	RefCLK+	Reference Clock non-inverted input, AC coupled on the host board – Not required	3
25	PECL-I	RefCLK-	Reference Clock inverted input, AC coupled on the host board – Not required	3
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TD-	Transmitter inverted data input	
29	CML-I	TD+	Transmitter non-inverted data input	
30		GND	Module Ground	1

Notes:

1. Module circuit ground is isolated from module chassis ground within the module.
2. Open connect should be pulled up with 4.7k – 10k ohm on host board to a voltage between 3.15V and 3.6V.
3. A Reference Clock input is not required.

Host Board Connector Pin Out

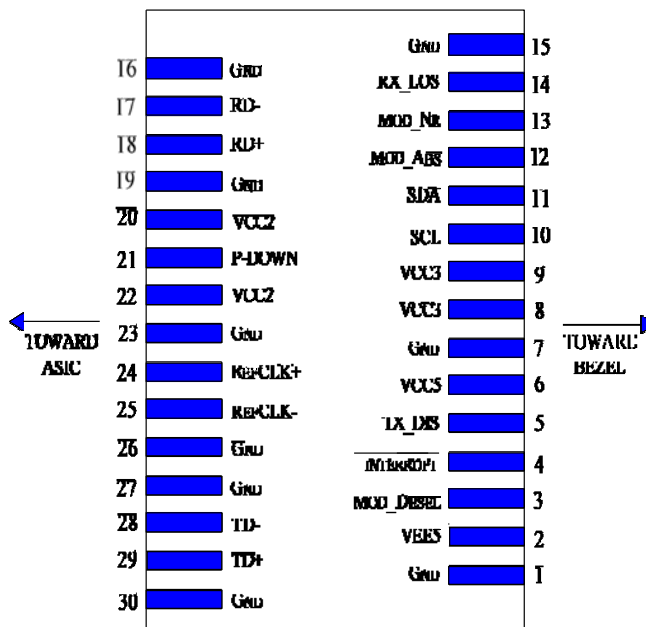


Diagram of Host Board Connector Block Pin Numbers and Name

General Specifications

Parameter	Symbol	Min	Typical	Max	Units	Note
Bit Rate	BR	9.95		11.1	Gb/s	
Bit Error Ratio	BER			10 ⁻¹²		1

Notes:

1. Tested 9.95G with 2³¹ – 1 PRBS pattern.

Digital Diagnostic Functions

Data Controls Inc's Small Form Factor 10Gbps (XFP) transceiver is compliant with the current XFP Multi-Source Agreement (MSA) Specification Rev 4.5.

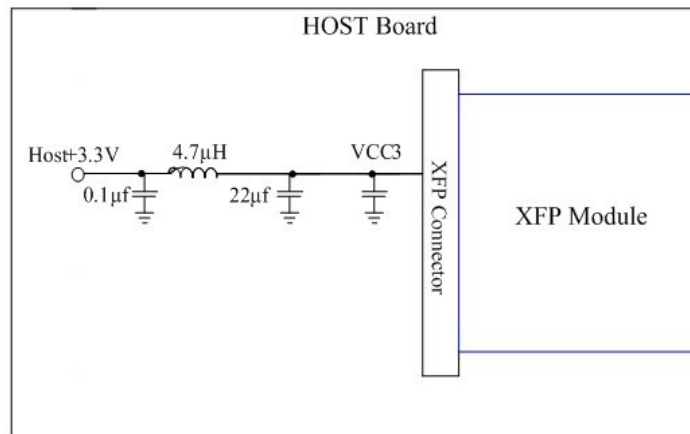
As defined by the XFP MSA, Data Controls Inc's XFP transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- ◆ Transceiver temperature
- ◆ Laser bias current
- ◆ Transmitted optical power
- ◆ Received optical power
- ◆ Aux Monitoring

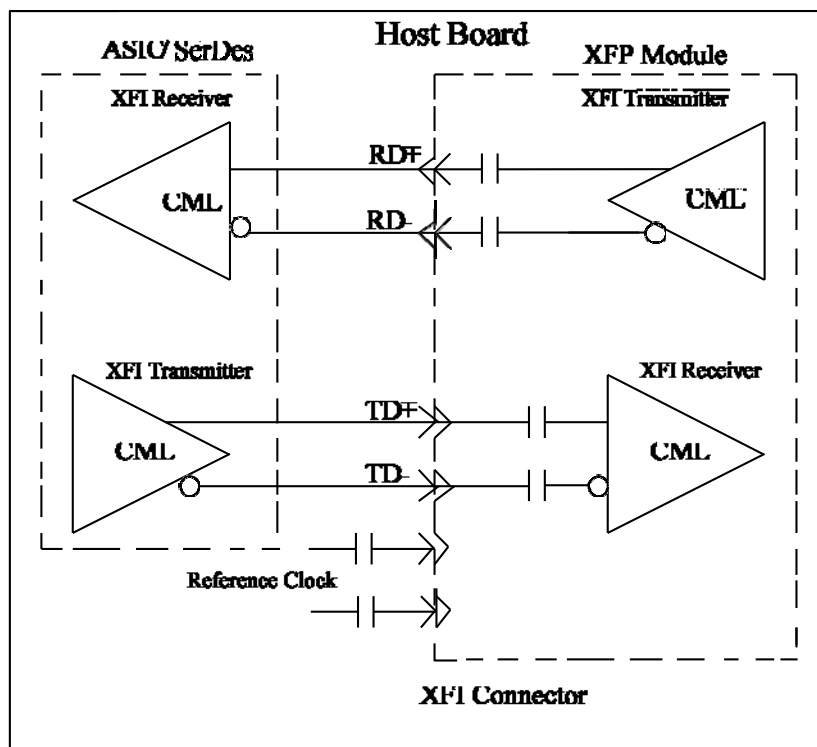
It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal range.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller inside the transceiver, which is accessed through the 2-wire serial interface. The serial data signal (SDA pin) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially. The 2-wire serial interface provides sequential or random access to the 8 bit parameters, addressed from 00h to the maximum address of the memory.

Recommended Host Board Power Supply Circuit

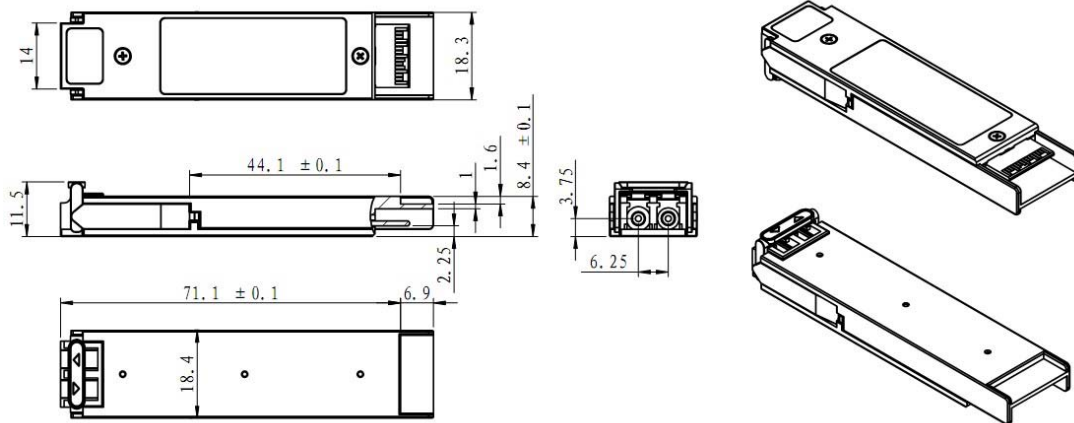


Recommended High-Speed Interface Circuit



Mechanical Specifications

Data Controls Inc's XFP transceivers are compliant with the dimensions defined by the XFP Multi-Sourcing Agreement (MSA).



Eye Safety

This single-mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

Obtaining Document

You can visit our website:

<https://www.dci.jp/>

Or contact Data Controls Inc.. listed at the end of the documentation to get the latest documents.

Revision History

Revision	Revision History	Release Date
V1.a	Released.	Mar 12, 2009
V2.a	Delete the item7 in Note2, because it is not compliant with ROHS6.	June 19, 2009
V3.a	Update sensitivity, Pout.	Sep 23, 2011
V3.b	Update LOS Deassert/Assert.	Sep 17, 2012
V3.c	Update wavelength range.	Mar 25,2014

Notice:

Data Controls Inc. reserves the right to make changes to or discontinue any optical link product or service identified in this publication, without notice, in order to improve design and/or performance. Applications that are described herein for any of the optical link products are for illustrative purposes only. Data Controls Inc. makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

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